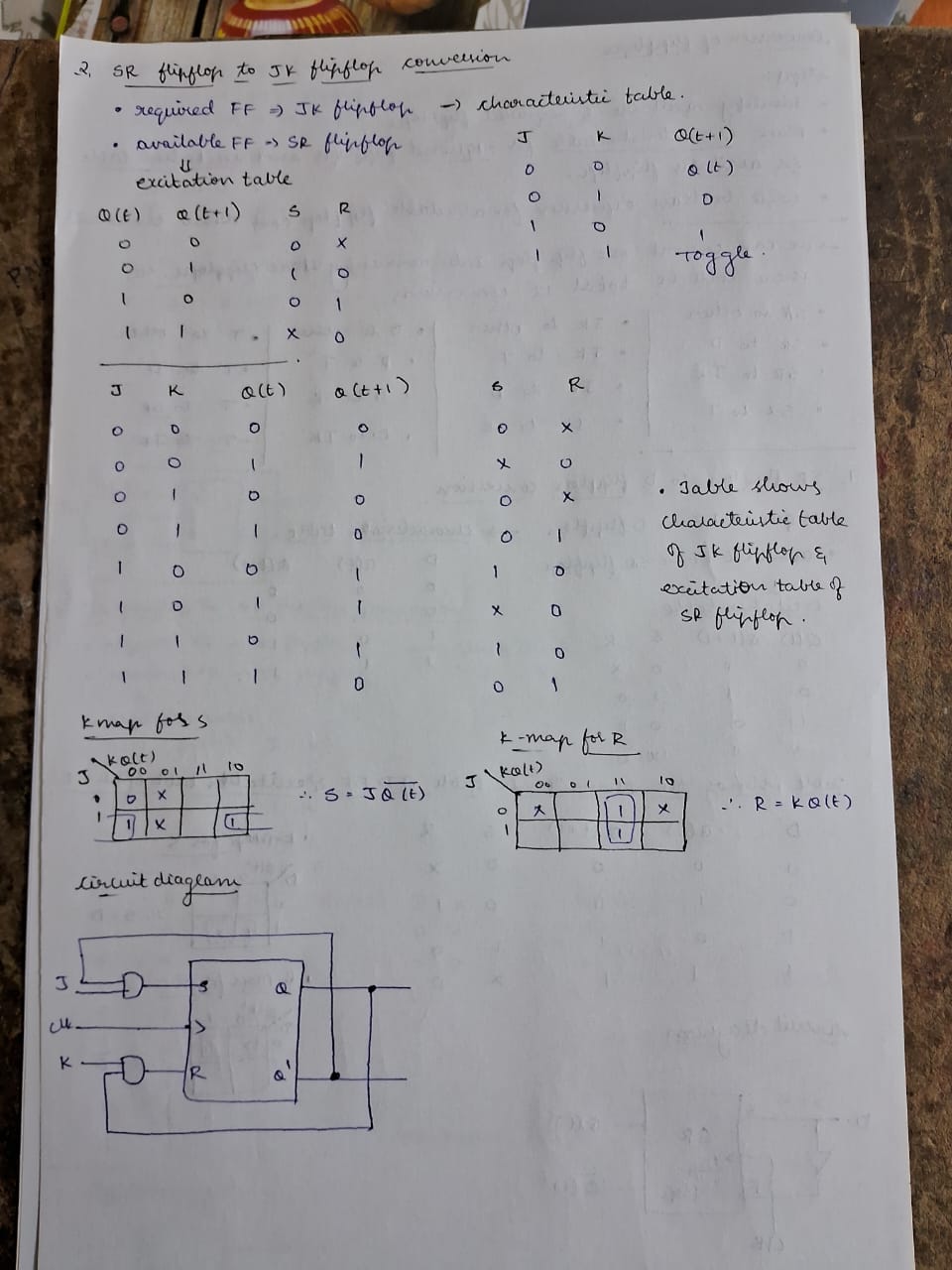
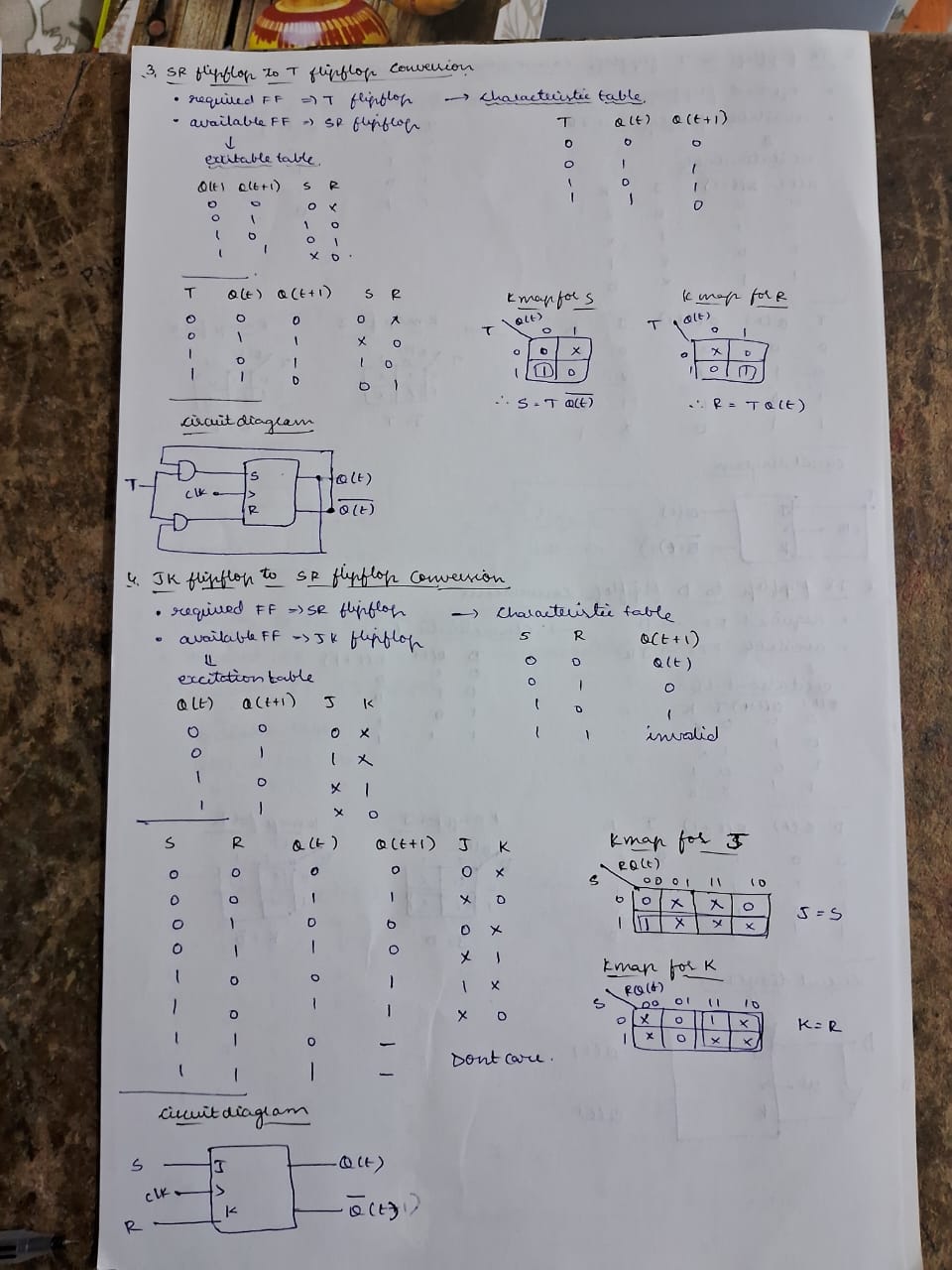


By Sri Pranathi Challagolla

**Conversion of Flip-flops**





A paper with writing on it

AI-generated content may be incorrect.

A piece of paper with writing on it

AI-generated content may be incorrect.

A piece of paper with writing on it

AI-generated content may be incorrect.

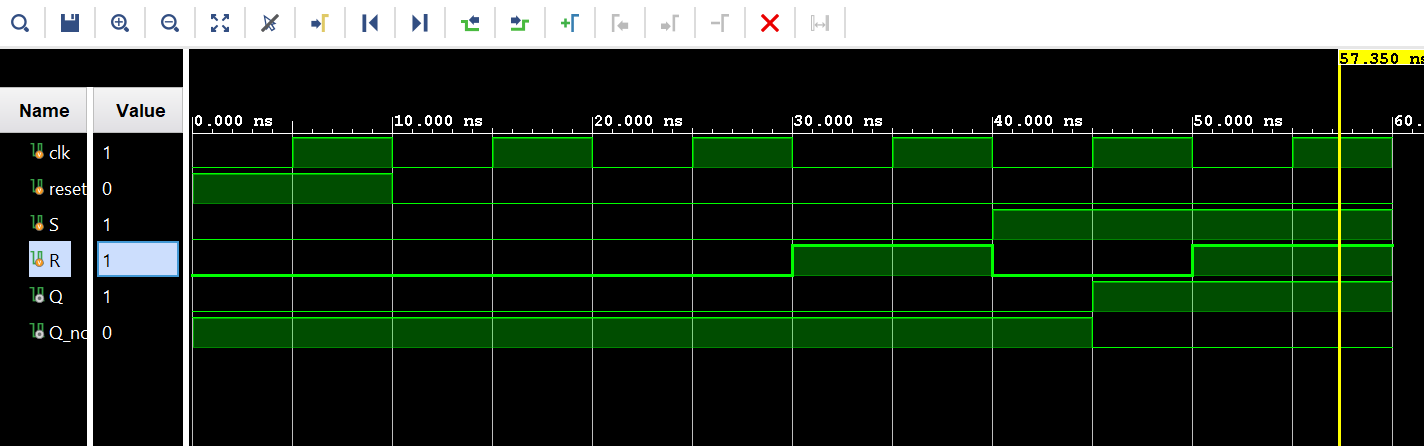
A piece of paper with writing on it

AI-generated content may be incorrect.

**Testbench:**

A screenshot of a computer program

AI-generated content may be incorrect.

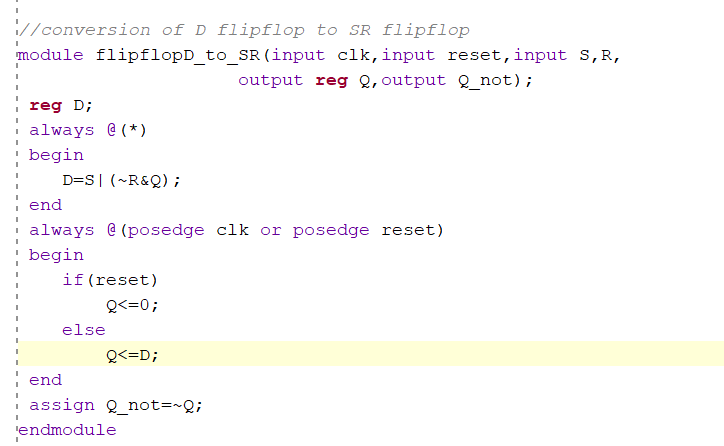


A diagram of a computer

AI-generated content may be incorrect.

**1.Implemented D flipflop to SR flipflop conversion using Verilog:**

**Design:**

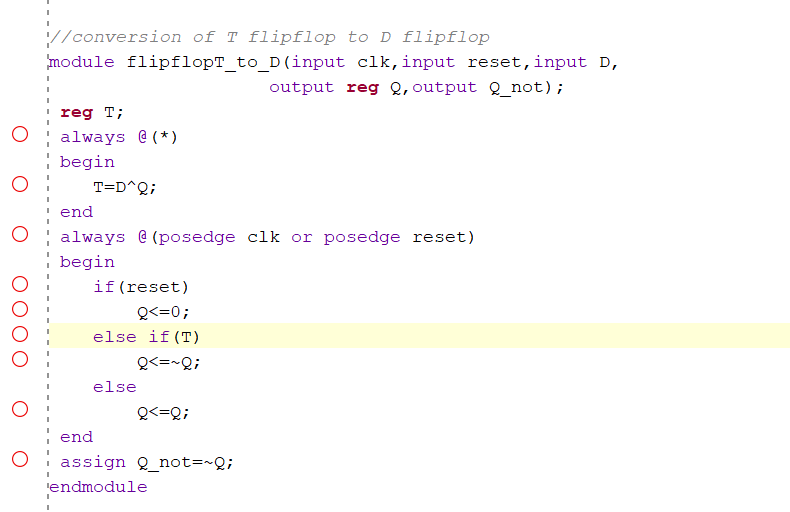


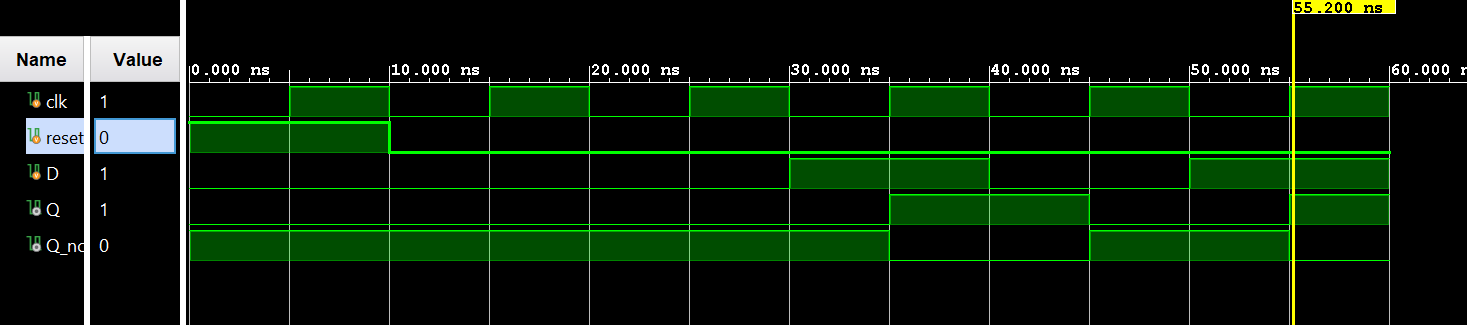
A diagram of a circuit

AI-generated content may be incorrect.

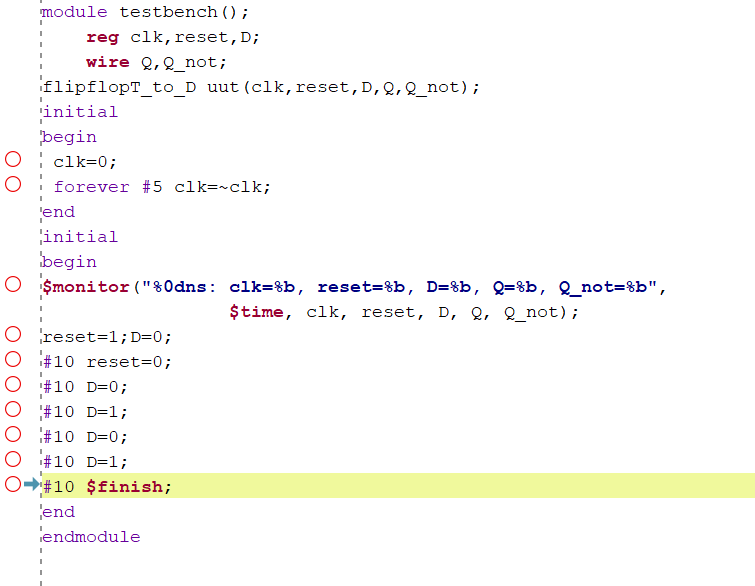
**2.Implemented T flipflop to D flipflop conversion using Verilog:**

**Design:**



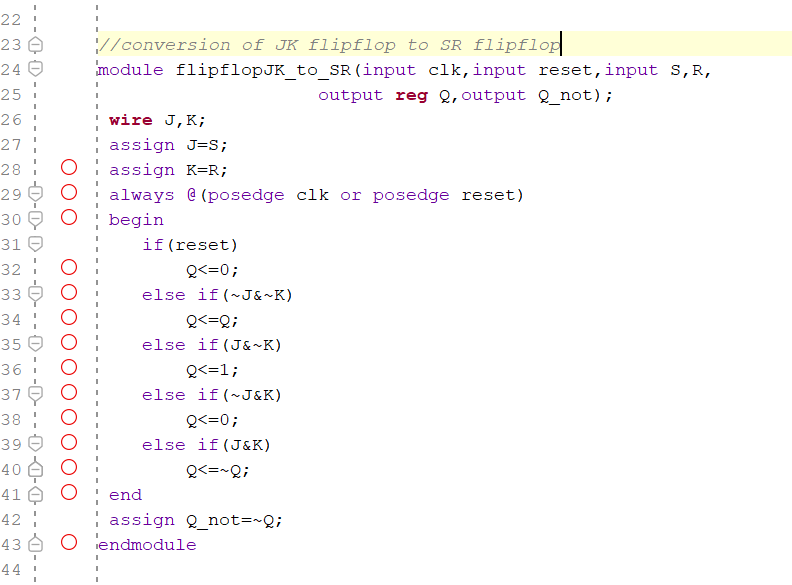


**Testbench:**

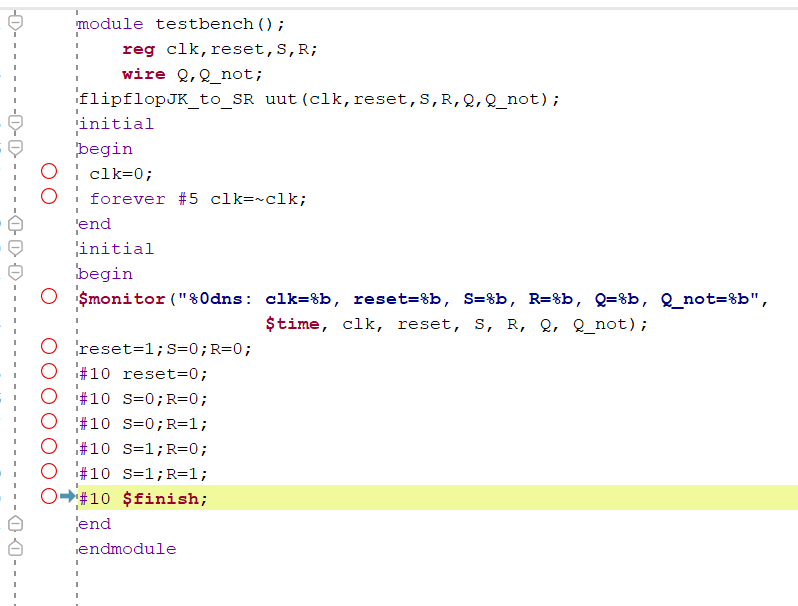


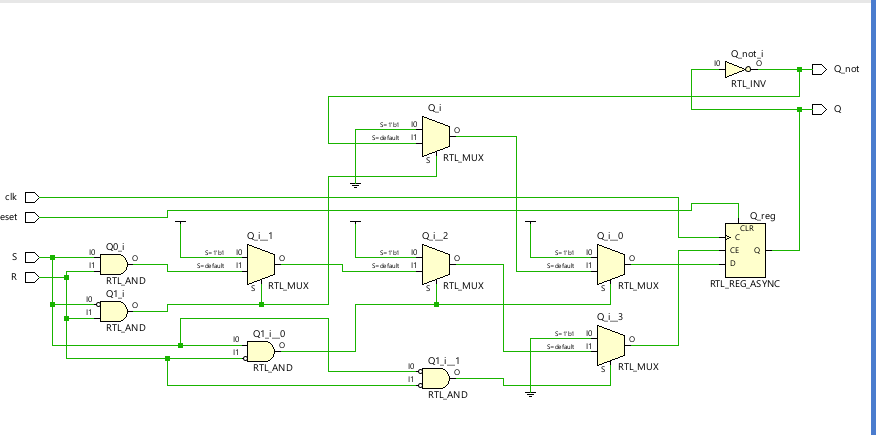
**3.Implemented JK flipflop to SR flipflop conversion using Verilog:**

**Design:**



**Testbench:**



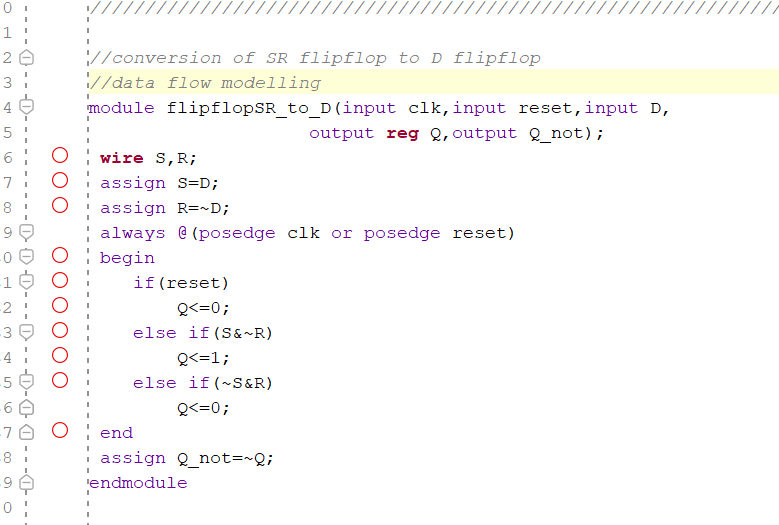


A screenshot of a computer

AI-generated content may be incorrect.

**4.Implemented SR flipflop to D flipflop conversion using Verilog:**

**Design:**



A diagram of a computer

AI-generated content may be incorrect.

A screen shot of a computer

AI-generated content may be incorrect.

**Testbench:**

A screenshot of a computer program

AI-generated content may be incorrect.